

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10992431-1

SERIAL NO.

APPLICANT

Frederick W. Pew et al

FILING DATE

Herewtih

GROUP

JC777 U.S. PTO
09/631036

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
DSL	1A	5,805,930	Sept. 8, 1998	Rosenthal et al	395	877
DSL	1B	5,978,892	Nov. 2, 1999	Noel et al	711	170
DSL	1C	6,049,854	Apr. 11, 2000	Bedarida	711	153
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

DSL	1Q	Patterson, David A., and John L. Hennessy. Computer Architecture: a quantitative approach, 2nd ed.. Morgan Kaufmann Publishers, Inc.: San Francisco, CA, 1996, page 527.
DSL	1R	Patterson, David A., and John L. Hennessy. Computer Organization and Design: the Hardware/software interface, 2nd ed. Morgan Kaufmann Publishers, Inc.: San Francisco, CA, 1998, pp 682-683.
DSL	1S	Definition for "Memory Management Unit" from "Free On-line Dictionary of Computing", http://wombat.doc.ic.ac.uk/foldoc/foldoc.cgi?query=mmu&action=Search (date site consulted: July 24, 2000).

EXAMINER

DATE CONSIDERED

11/5/03